



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
 Address: COMMISSIONER FOR PATENTS
 P.O. Box 1450
 Alexandria, Virginia 22313-1450
 www.uspto.gov



CONFIRMATION NO. 4944

Bib Data Sheet

SERIAL NUMBER 10/757,395	FILING OR 371(c) DATE 01/15/2004 RULE	CLASS 327	GROUP ART UNIT 2816	ATTORNEY DOCKET NO. 108075-00122
-----------------------------	--	--------------	------------------------	--

APPLICANTS

Yasuhige Ogawa, Kasugai-shi, JAPAN;
 Yoshiyuki Ishida, Kasugai-shi, JAPAN;

** CONTINUING DATA *****

This application is a DIV of 09/642,937 08/22/2000 PAT 6,700,437

** FOREIGN APPLICATIONS *****

JAPAN 2000-004773 01/13/2000

Ken
OK

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 04/19/2004

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 13	TOTAL CLAIMS 5	INDEPENDENT CLAIMS 1
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged <i>Kenji T. Weller</i> Examiner's Signature	Initials			

ADDRESS

004372

TITLE

Semiconductor device having logic circuit and macro circuit

FILING FEE RECEIVED 770	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
----------------------------	---	---